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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10052306	FILING DATE 01/18/2002	CLASS 438	SUBCLASS 677	GAU 2813	EXAMINER E. KIELIN
**APPLICANTS: Kawano Yumiko; Yamasaki Hideaki; Chung Gishi;					
**CONTINUING DATA VERIFIED: <i>EK</i> THIS APPLICATION IS A CON OF PCT/JP00/04889 07/21/2000					
** FOREIGN APPLICATIONS VERIFIED: <i>EK</i> JAPAN H11-210149 07/26/1999					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO	
Verified and Acknowledged Examiners's initials <i>EK</i>				2285/51302	
TITLE : Method and apparatus for manufacturing semiconductor device					

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
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